

FIG. 1

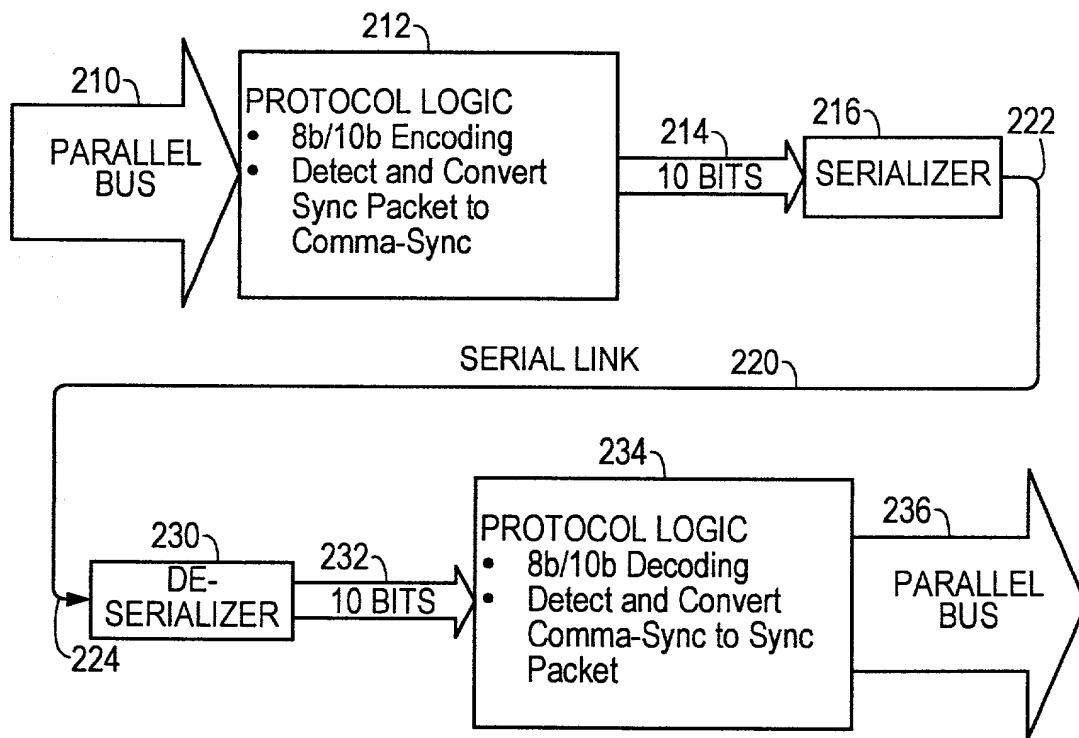


FIG. 2

FIG. 3 is a block diagram of a system 300 for data transfer between two parallel buses. The system includes a first parallel bus 210, a second parallel bus 236, an 8b/10b encoder 212, an 8b/10b decoder 234, and a serial link 220. The first parallel bus 210 is connected to the 8b/10b encoder 212. The 8b/10b encoder 212 is connected to the serial link 220. The serial link 220 is connected to the 8b/10b decoder 234. The 8b/10b decoder 234 is connected to the second parallel bus 236. The system 300 is configured to transfer data from the first parallel bus 210 to the second parallel bus 236 via the serial link 220. The data transfer process involves encoding data from the first parallel bus 210 into 8b/10b format by the encoder 212, transmitting the encoded data over the serial link 220, and then decoding the received data back into 8b/10b format by the decoder 234, which is then transferred to the second parallel bus 236. The diagram shows a sequence of operations: data transfer from bus 210, generation of a sync packet, data transfer to bus 236, and then a return path from bus 236 to bus 210.

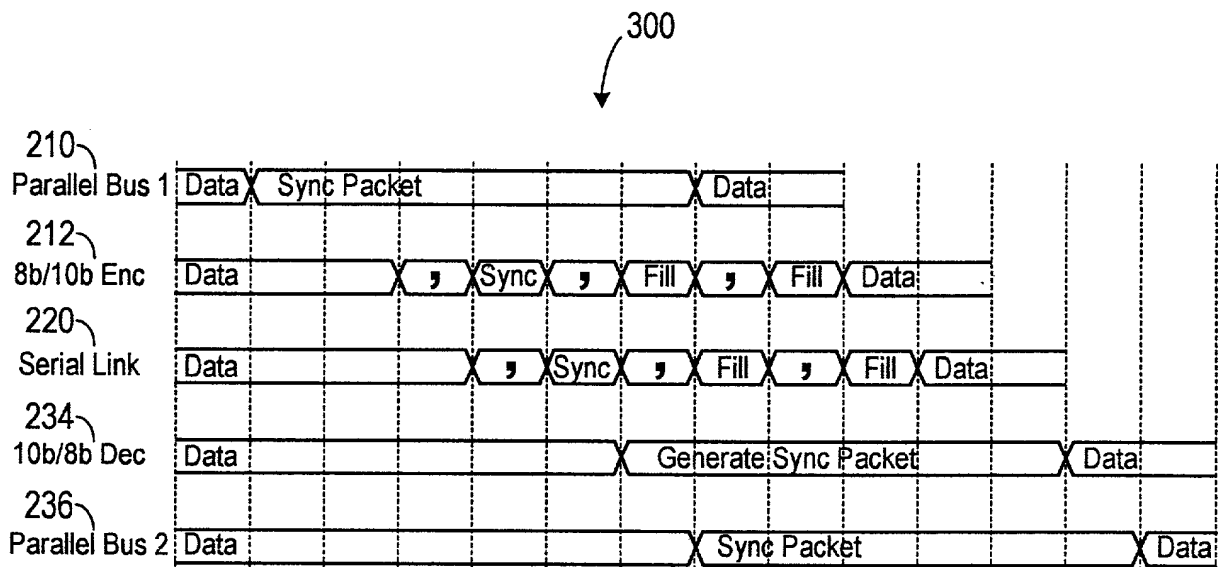


FIG. 3